



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Cobbley et al.

Serial No.: 10/714,188

Filed: November 14, 2003

For: METHOD FOR PACKAGING FLIP-CHIP SEMICONDUCTOR ASSEMBLIES

Confirmation No.: 9360

Examiner: B. Owens

Group Art Unit: 2824

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

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Sir:

In compliance with the duty to disclose information material to patentability pursuant to 37 C.F.R. § 1.56, it is respectfully requested that this Supplemental Information Disclosure Statement be entered and the document listed on attached Form PTO/SB/08 be considered by the Examiner and made of record. A copy of the listed document is not being provided pursuant to M.P.E.P. 609 III A(2). The listed document was cited by the Office in co-pending application Serial No. 10/693,286, filed on October 23, 2003, and directed to a related invention.

In accordance with 37 C.F.R. § 1.97(g) and (h), filing of this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or an

Serial No. 10/714,188

admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, no representation is made by Applicants herein that no other possible material information as defined in 37 C.F.R. § 1.56 (b) exists.

U.S. Patent Documents

U.S. Patent No.

Publication Date

Patentee

5,440,241

08/08/1995

King et al.

Applicants offer to supply any explanation or discussion of the document which the Examiner feels is necessary or desirable and which is requested.

This Supplemental Information Disclosure Statement is believed to be filed before the mailing date of the first Office Action on the merits subsequent to the filing of an RCE in the above-identified application, and therefore no additional fee is due.

Respectfully submitted,



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Enclosures: Form PTO/SB/08

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